

FIGURE 1
(PRIOR ART)

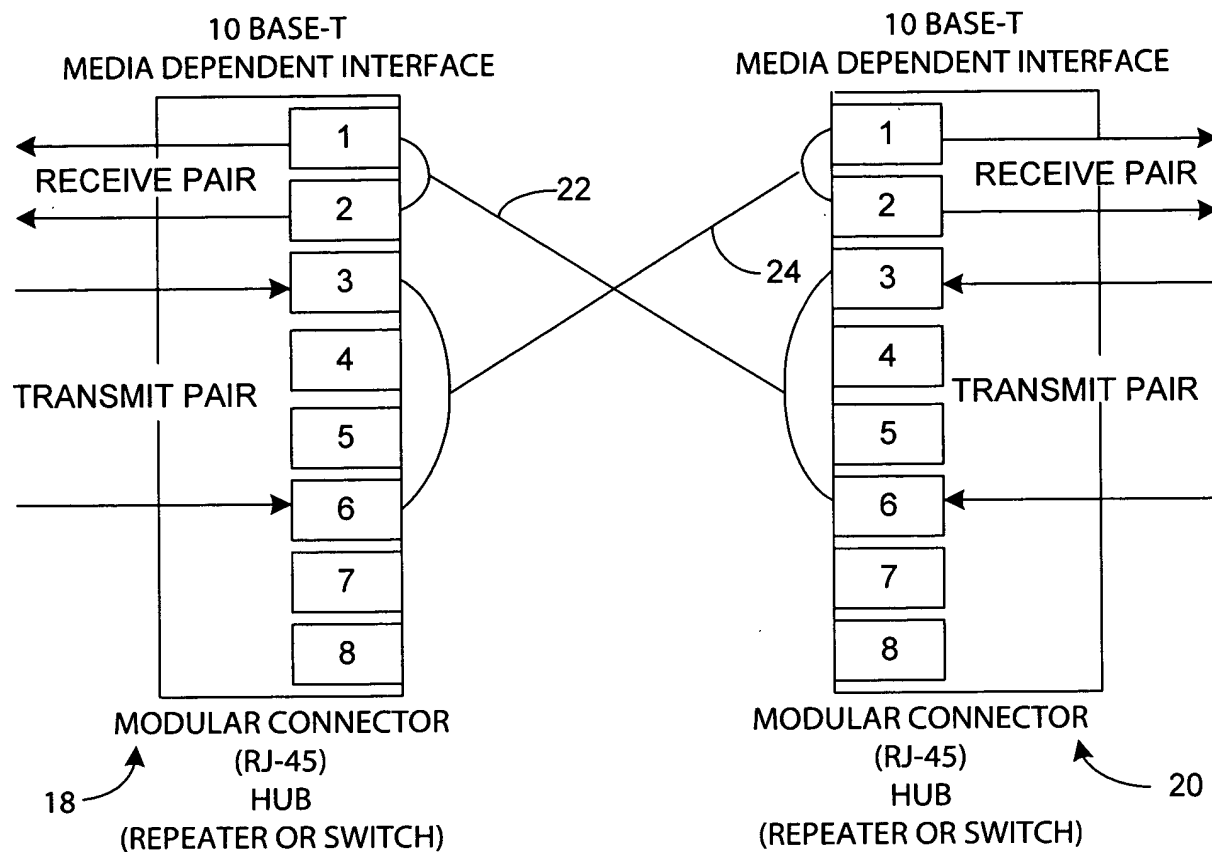


FIGURE 2
(PRIOR ART)

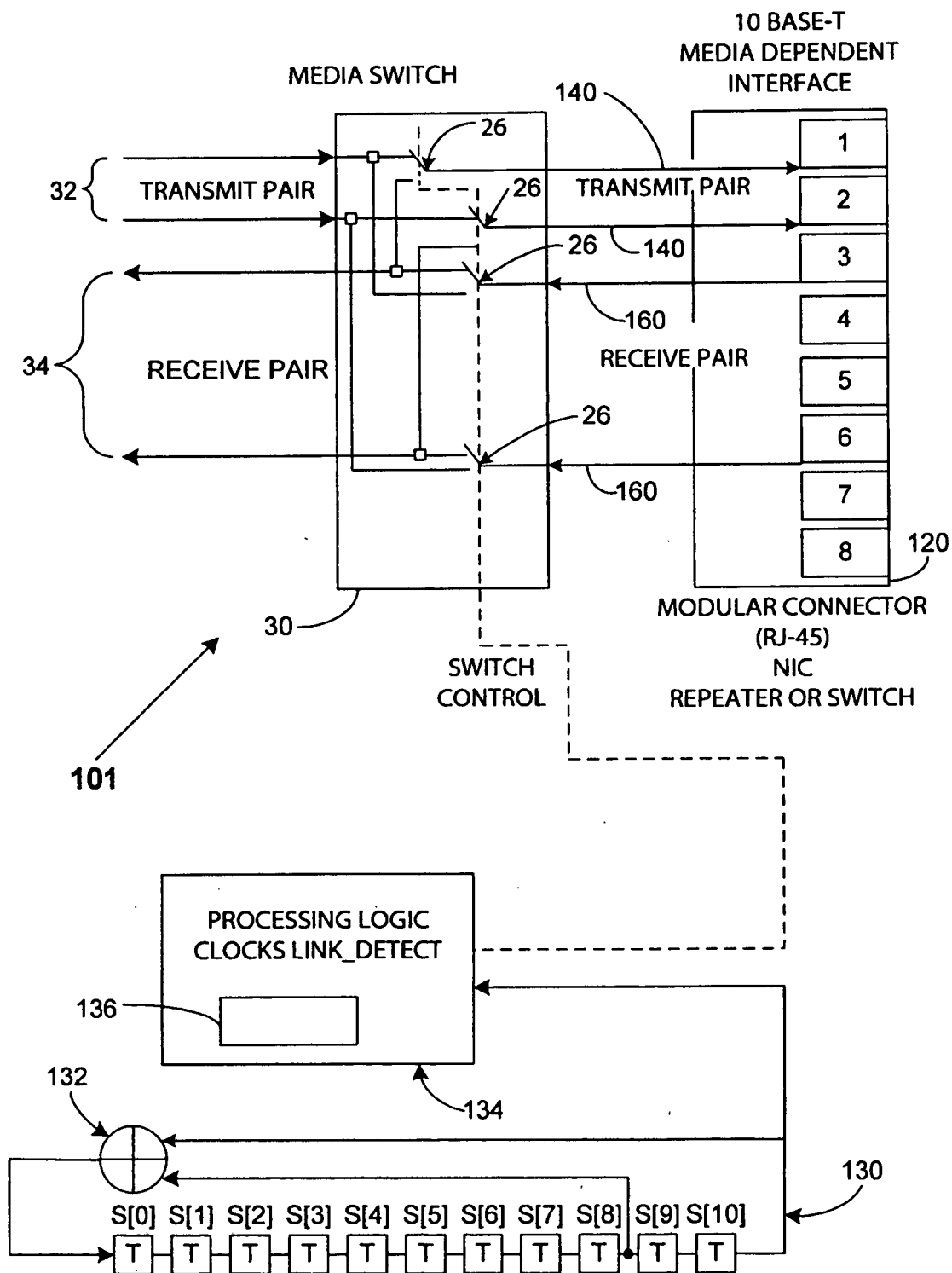
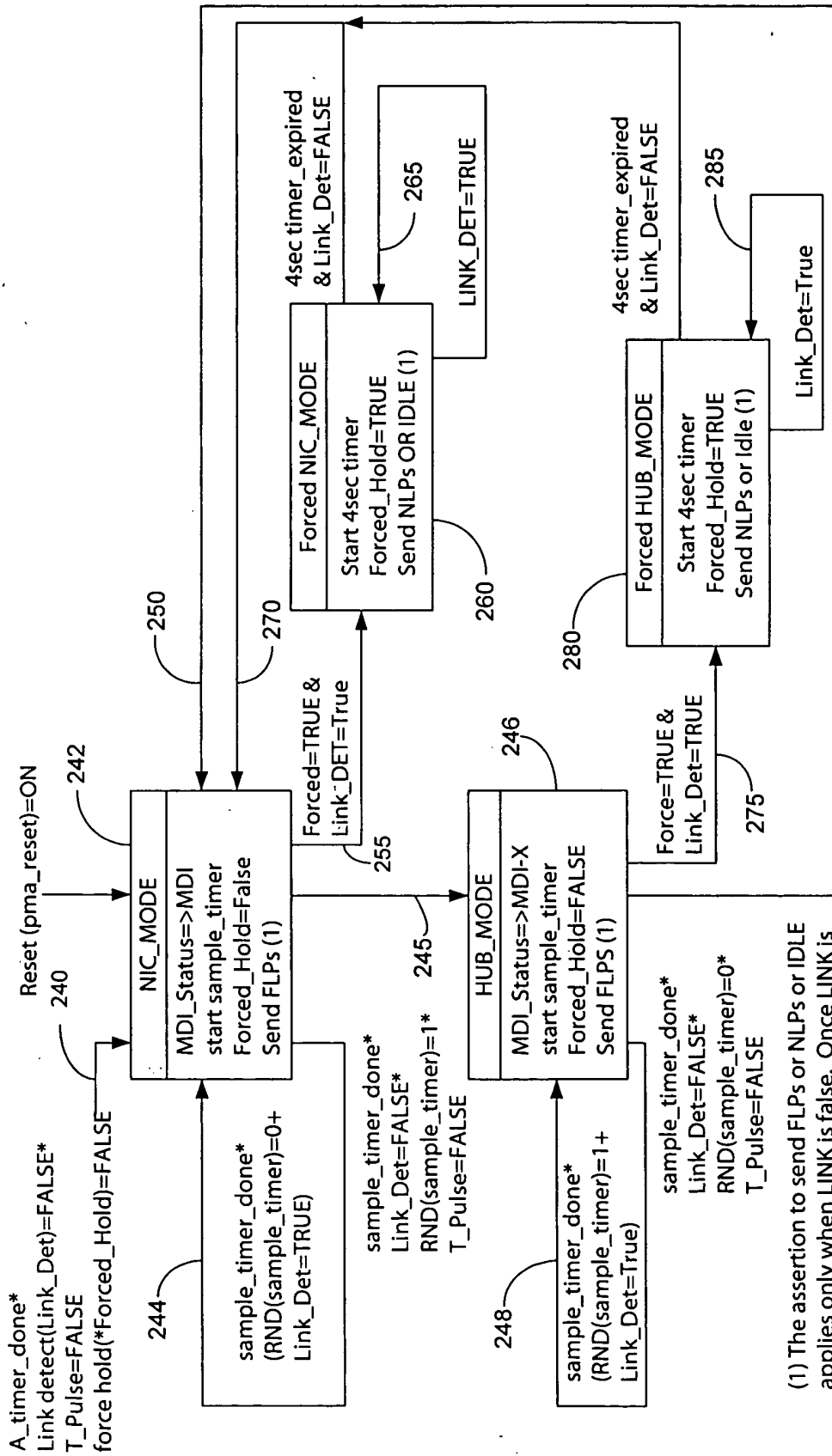


FIGURE 3



Forced Mode Auto-MDIX State Machine

FIGURE 5

(1) The assertion to send FLPs or NLPs or IDLE applies only when LINK is false. Once LINK is established, DATA may be transmitted.

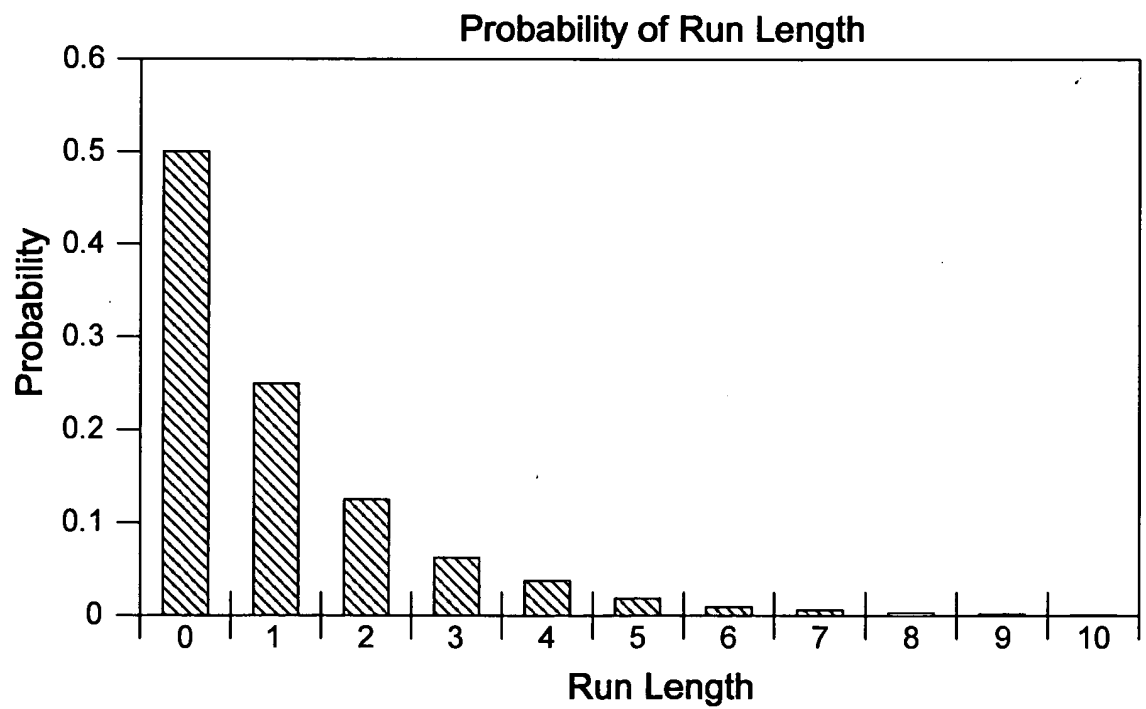


FIGURE 6